



FORM PTO-1449	SERIAL NO. 10/023,200	CASE NO. 10519/24
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE December 14, 2001	GROUP ART UNIT 2133 2848
(use several sheets if necessary)		APPLICANT(S): MARCH ET AL.

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
Pme	A1	6,236,587	05/22/01	GUDESEN ET AL.		
	A2	6,034,882	03/07/00	JOHNSON ET AL.		
	A3	5,835,396	11/10/98	ZHANG		
	A4	5,784,391	07/21/98	KONIGSBURG		
	A5	5,469,450	11/21/95	CHO ET AL.		
	A6	5,313,425	05/17/94	LEE ET AL.		
	A7	4,646,266	02/24/87	OVSHINSKY ET AL.		
	A8	6,216,247	04/10/01	CRETA ET AL.		
	A9	6,185,122	02/06/01	JOHNSON ET AL.		
	A10	6,016,269	01/18/00	PETERSON ET AL.		
	A11	5,943,254	08/24/99	BAKEMAN, JR. ET AL.		
	A12	5,835,509	11/10/98	SAKO ET AL.		
	A13	5,796,694	08/18/98	SHIRANE		
	A14	5,708,667	01/13/98	HAYASHI		
	A15	5,432,729	07/11/95	CARSON ET AL.		
Pme	A16	5,872,790	02/16/99	DIXON		

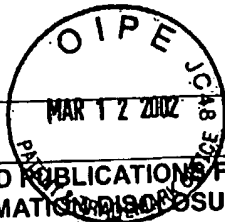
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO
	A17	WO 99/14763	03/25/99	PCT			

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
Pme	A18	"Computer Engineering: Hardware Design," M. Morris Mano, Chapter 6-4 Error Detection and Correction, pages 199-202 (1988).
Pme	A19	"Reed-Solomon Codes," http://www.4i2i.com/reed_solomon_codes.htm , 8 pages (1998).
Pme	A20	"The Norton Desktop (Version 3 For Windows) User's Guide," pp. 16-1 - 16-6 (1993).
Pme	A21	"Exotic Memories, Diverse Approaches," EDN Asia, pages 22-33 (September 2001).
Pme	A22	"A Vertical Leap for Microchips," Thomas H. Lee, Scientific American, 8 pages (January 2002; printed December 10, 2001).
	A23	"Three-Dimensional Memory Array and Method of Fabrication," U.S. Patent Application Serial No. 09/560,626, filed April 28, 2000; inventor: Johan Knall.
	A24	"Write-Once Memory Array Controller, System, and Method," U.S. Patent Application Serial No. 09/638,427, filed August 14, 2000; inventors: Derek J. Bosch, Christopher S. Moore, Daniel C. Steere, and J. James Tringali.

EXAMINER <i>Phung Chung</i>	DATE CONSIDERED <i>9/16/04</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE December 14, 2001	GROUP ART UNIT 2133 2010
APPLICANT(S): March et al.		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
	A25 "Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,428, filed August 14, 2000; inventors: Mark G. Johnson, Thomas H. Lee, Vivek Subramanian, and P. Michael Farmwald.
	A26 "Modular Memory Device," U.S. Patent Application Serial No. 09/638,334, filed August 14, 2000; inventors: J. James Tringali, P. Michael Farmwald, Thomas H. Lee, Mark G. Johnson, and Derek J. Bosch.
	A27 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. 09/748,589, filed December 22, 2000; inventors: Roger W. March, Christopher S. Moore, Daniel Brown, Thomas H. Lee, and Mark G. Johnson.
	A28 "Three-Dimensional Memory Array and Method for Storing Data Bits and ECC Bits Therein," U.S. Patent Application Serial No. 09/747,574, filed December 22, 2000; inventors: Thomas H. Lee, James M. Cleaves, and Mark G. Johnson.
	A29 "Method for Deleting Stored Digital Data from Write-Once Memory Device," U.S. Patent Application Serial No. 09/638,439, filed August 14, 2000; inventors: Christopher S. Moore, Derek J. Bosch, Daniel C. Steere, and L. James Tringali.
	A30 "Solid State Memory Device Storing Program Code and Methods for Use Therewith," U.S. Patent Application Serial No. 09/775,745, filed February 2, 2001; inventors: Christopher S. Moore, Roger March, Dan Brown.

Not Prior Arts

EXAMINER <i>Phung Chung</i>	DATE CONSIDERED <i>2/16/04</i>
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CLIENTS AND PUBLICATIONS

REFERENCE DESIGNATION	U.S. PATENT DOCUMENTS
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3	1,100,000
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	

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